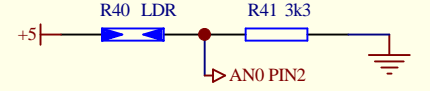


IF 1Hz / 50Hz / 60 Hz:
 Pin4 = Blink 1Hz
 Pin5 = Blink 0.5Hz
 Pin7 = AM/PM indicator
 It is optional to use - those outputs.
 X1 4MHz Resonator
 C30 and C31 not mounted.

IF DCF77 Option:
 D30 is Dual LED GREEN/RED signal indicator.
 D31 is a normal RED led. Change 1min = DCF OK,
 Fast blink = DCF bad for 24hrs warning.
 X1 4.000000 Crystal 30ppm
 C30 and C31 crystal loading, ceramic NP0/COG type

The software from 5.7 for this version PCB 1.08 has autodetection of all four clock input modes !!
 If there is a LOW pulse signal on the DCF77 input pin, while powering up, it will go into DCF mode,
 else it will measure the input clock frequency and set right divider mode. Please read documentation for more information about how the different clock modes work

LDR OPTION (NOT INCLUDED IN KIT)



LDR option is automatically disabled if voltage is over 4.7

AN0 voltage	DIMM level
2.94 - 4.7V	3 = Max (fading mode)
2.25 - 2.74V	2
1.47 - 2.15V	1
0.78 - 1.37V	0 = Min
0.11 - 0.76	Reserved
0.0 - 0.1	S2 switch active

To adjust light levels to your need, simply adjust R41 and R16 values a bit

Title		NIXIE CLOCK OZ2CPU 2x3 MUX = 6 digits	
Thomas Scherrer & Claus Urbach (webx.dk & nixieclocks.de)			
Size	Number	Revision	
B	Schematic for nixie clock PCB 1.08	8c-6	
Date:	11-Nov-2007	Sheet 1 of 1	
File:	C:\108sm.ddb	Drawn By: OZ2CPU Thomas Scherrer	